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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/715,600	11/17/2000	James A. Schinnerer	10005282-1	6675

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HEWLETT-PACKARD COMPANY
Intellectual Property Administration
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EXAMINER

RAHMJOO, MANUCHER

ART UNIT	PAPER NUMBER
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2676

DATE MAILED: 07/11/2003

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Please find below and/or attached an Office communication concerning this application or proceeding.

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Office Action Summary

Application No.

09/715,600

Applicant(s)

SCHINNERER ET AL.

Examiner

Mike Rahmjoo

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).
- Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 11/17/2000.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-19 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1-19 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
- Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
- 11) ☐ The proposed drawing correction filed on _____ is: a) ☐ approved b) ☐ disapproved by the Examiner.
- If approved, corrected drawings are required in reply to this Office action.
- 12) ☐ The oath or declaration is objected to by the Examiner.

Priority under 35 U.S.C. §§ 119 and 120

- 13) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
- ☐ Certified copies of the priority documents have been received.
 - ☐ Certified copies of the priority documents have been received in Application No. _____.
 - ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- * See the attached detailed Office action for a list of the certified copies not received.
- 14) ☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. § 119(e) (to a provisional application).
- a) ☐ The translation of the foreign language provisional application has been received.
- 15) ☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. §§ 120 and/or 121.

Attachment(s)

- 1) ☒ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☒ Information Disclosure Statement(s) (PTO-1449) Paper No(s) 3.
- 4) ☐ Interview Summary (PTO-413) Paper No(s). _____
- 5) ☐ Notice of Informal Patent Application (PTO-152)
- 6) ☐ Other:

DETAILED ACTION

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claims 1- 19 are rejected under 35 U.S.C. 102(b) as being anticipated by Garcia, US Patent 5,510,832, hereinafter, Garcia.

As per claims 1,11,15, and 17 Garcia teaches receiving the active stereo video data containing the right channel pixel data and the left channel pixel data corresponding to the image to be rendered see for example column 6 lines 16- 38; re-sequencing (field displacement and offsetting) the right channel pixel data and the left channel pixel data see for example claim 7b and in column 13 lines 45- 59; and simultaneously outputting corresponding frames of the right channel pixel data and the left channel pixel data for displaying the image to be rendered in passive stereo see for example column 6 lines 17- 32.

As per claims 2 and 12 Garcia teaches a frame rate of the pixel data simultaneously

output for displaying the image to be rendered in passive stereo is approximately one half of a frame rate of the pixel data of the active stereo video data see for example column 11 lines 1- 15 and column 12 lines 1- 11.

As per claims 3 and 18 Garcia teaches receiving the active stereo video data from multiple digital video data streams, each of the multiple digital video data streams being provided by a graphics pipeline, each graphics pipeline being configured to process pixel data corresponding to at least a portion of the image to be rendered see for example in column 11 lines 56- 67 and figure 5 through stream of sequential video frames.

As per claims 4 and 13 Garcia teaches providing a first left channel frame buffer, a second left channel frame buffer, a first right channel frame buffer, and a second right channel frame buffer; and wherein the step of re-sequencing the right channel pixel data and the left channel pixel data comprises the step of: allocating the right channel pixel data and the left channel pixel data to the first left channel frame buffer, the second left channel frame buffer, the first right channel frame buffer, and the second right channel frame buffer see for example column 12 lines 11- 34 through sequential frames of data being buffered.

As per claims 5,14,16 and 19 Garcia teaches buffering a first frame of the right channel pixel data; buffering a first frame of the left channel pixel data; simultaneously providing the first frame of the right channel pixel data and the first frame of the left channel pixel data for displaying the image to be rendered; determining whether a second frame of the right channel pixel data and a second frame of left channel pixel data are ready for simultaneously providing; and if the second frame of the right channel pixel data and the second frame of left channel pixel data are not ready for simultaneously providing, again simultaneously providing the first frame

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of the right channel pixel data and the first frame of the left channel pixel data see for example column 12 lines 11- 34 while the next input frame data is being acquired, the previously acquired and stored frames are displayed through sequential frames of data which are buffered.

As per claim 6 Garcia teaches receiving a first of the multiple digital video data streams containing three dimensional pixel data corresponding to the image to be rendered; and receiving a second of the multiple digital video data streams containing two dimensional pixel data corresponding to the image to be rendered in column; and combining the two-dimensional pixel data and the three-dimensional pixel data see for example column 5 lines 20- 37.

As per claim 7 Garcia teaches replacing at least a portion of the pixel data provided by the second of the multiple digital video data streams with at least a portion of the pixel data provided by the first of the multiple digital video data streams see for example column 6 lines 4- 15 and column 11 lines 25- 47.

As per claim 8 Garcia teaches utilizing chroma- key values for allocating the right channel pixel data and the left channel pixel data see for example the abstract and column 9 lines 63- 67 and column 10 lines 1- 3.

As per claims 9 and 10 Garcia teaches utilizing over scanned information contained in the active stereo video data for allocating the right channel pixel data and the left channel pixel data and one of various left and right channel buffers see for example column 11 lines 25- 47 and column 12 lines 12- 34.